

# Calculation of Arc-Circuit Asymmetry in Electric-Arc Furnaces

**Peter F. Ryff**  
Ryerson University

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# Calculation of arc-circuit asymmetry in electric-arc furnaces

Prof. P.F. Ryff, M.A.Sc., Ph.D., Sen. Mem. I.E.E.E., Member A.P.E.O.

*Indexing terms:* Arcing, Industrial applications of power

**Abstract:** A method is described to determine the arc voltages on a per-phase basis of a 3-phase direct electric-arc furnace. Having determined the arc voltages, other arc parameters, such as arc resistance, length and power, can then be obtained. By using the procedure outlined, in conjunction with experimental data on a 100 t, 5½ m diameter shell furnace having a triangulated secondary electrical system, it will be shown that controlling arc powers by means of the impedance control method still results in asymmetric furnace operation. Sample calculations are included, illustrating actual conditions for this furnace.

## 1 Introduction

The uneven arc-power levels at the tips of the electrodes during furnace operation is a well known fact, and they generally result in imbalanced circuit conditions. Ideally, the arc voltage and current should be measured and their product representing the arc power controlled by the electronic control system, in order to ensure even powers. However, direct measurement of the arc voltage cannot be carried out during operation, for obvious reasons, such as the furnace atmosphere and the fact that the system has a floating neutral. Therefore, the current and voltage is measured at the furnace transformer and the resulting impedance  $Z = V/I$  taken as an operational value. Electrode control systems operating on this scheme are referred to as 'impedance controlled'. Arc powers are controlled by the total phase impedance, regardless of their phase angle. Provided the system is linear and the secondary circuit is ideally triangulated, the phases can be constructed such that they are electrically equal. The operation of an arc furnace, however, is more complicated. This is mainly because of two separate phenomena, one being the impedance of the furnace, the other attributable to the arc itself. The secondary circuit of a modern arc furnace is triangulated throughout. If no restrictions are placed by the meltshop, such as height requirements, equilateral triangulation is strived for. However, on the larger furnaces there are several parallel flexible cables, as well as parallel bus tubes on the electrode arms. Thus, having multiple conductors in parallel, per-phase various geometrical configurations are possible, none constituting ideal equilateral triangular systems. For instance, for the furnace under discussion there are four flexible cables and two bus tubes per phase, and the triangulation employed is as shown in Fig. 1. As can be seen, the distances between the cables, and the bus tubes are not all equal and, therefore, their mutual inductances vary. Consequently, the phase resistances and reactances are unequal, giving rise to asymmetry in the furnace, ultimately exhibiting itself in uneven side-wall wear of the furnace lining. Naturally efforts are made to minimise any resulting imbalance, but ideal conditions are not achieved. Furthermore, it must be realised that the resistances and reactances of the individual phases are not really constant. This is owing to unavoidable changes in circuit configuration caused by uneven charging levels under the electrodes, moving the electrode arms from the desired triangulated configuration. At the same time, the phase currents continually change, resulting in movement of the flexible cables in response to changing electromagnetic forces. Cables in the phases are

pulled in a tighter configuration, while at the same time the cables between phases swing apart when, for instance, the current increases. These combined effects modify the reflected resistances and coupled reactances continuously, thereby in turn altering the phase impedances [1]. In addition, the electric arcs themselves introduce nonlinearities, especially during the initial period of a heat, since their voltage waveforms are rarely sinusoidal. Since the arc resistance depends upon the arc voltage, the impedance is affected even more.

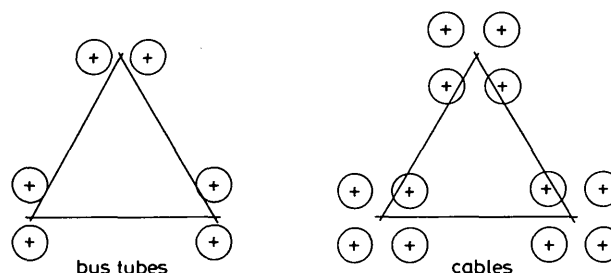


Fig. 1 Schematic of triangulation of furnace secondary electrical circuit

In this paper, a procedure is outlined to determine the arc voltages. Knowing these, all other arc-circuit parameters can then be determined. As a specific example, the results are applied to a furnace installation for which numerous experimental data are available. It will be shown that, in this case, representing a typical installation of good design, the imbalance, although modest, is present.

## 2 Derivation of arc-voltage equation

The secondary electrical circuit of an electric-arc furnace from the transformer secondary tap on can be schematically represented by an electric circuit, as illustrated in Fig. 2. The phase impedances  $Z_i = r_i + jx_i$ ,  $i = 1, 2, 3$  consist of the equivalent resistances and reactances per phase, which accounts for the mutual inductances and reflected resistances. This representation of the equivalent circuit is applicable for all furnaces, whether balanced or not.  $V_i$  and  $I_i$ ,  $i = 1, 2, 3$ , denote the phase voltages and currents, respectively. Applying Kirchoff's laws, the following relations can be stated:

$$\bar{V}_1 - \bar{V}_2 = \bar{I}_1 \bar{Z}_1 - \bar{I}_2 \bar{Z}_2 + \bar{v}_1 - \bar{v}_2 \quad (1)$$

$$\bar{V}_2 - \bar{V}_3 = \bar{I}_2 \bar{Z}_2 - \bar{I}_3 \bar{Z}_3 + \bar{v}_2 - \bar{v}_3 \quad (2)$$

$$\bar{I}_1 + \bar{I}_2 + \bar{I}_3 = 0 \quad (3)$$

where  $v_i$ ,  $i = 1, 2, 3$ , represents the arc-voltage drops across the arc resistances  $R_i$ , respectively, in the corresponding phases.

Considering the phase relationships between the various

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The author is with the Ryerson Polytechnical Institute, Department of Electrical Engineering Technology, 50 Gould St., Toronto, Ont., Canada M5B 1E8

electrical parameters, as shown in Fig. 3, eqn. 1 can be written as

$$V_1 - V_2(\cos \phi_2 + j \sin \phi_2) = I_1(\cos \alpha_1 + j \sin \alpha_1) \times (r_1 + jx_1) - I_2(\cos \alpha_2 + j \sin \alpha_2)(r_2 + jx_2) + v_1(\cos \alpha_1 + j \sin \alpha_1) - v_2(\cos \alpha_2 + j \sin \alpha_2) \quad (1a)$$

where  $\phi_i$  and  $\alpha_i$ ,  $i = 1, 2, 3$ , are the angles associated with the various electrical quantities, as illustrated in Fig. 3. The arc voltage and current are assumed to be in phase. Eqns. 2 and 3 can be similarly treated. Separating the resulting equations into their real and imaginary components, realising that

$$r_i = Z_i \cos \alpha_{z_i} \quad x_i = Z_i \sin \phi_{z_i}$$

and making use of the trigonometric identity

$$\cos(\alpha_i + \phi_{z_i}) = \cos \alpha_i \cos \phi_{z_i} - \sin \alpha_i \sin \phi_{z_i}$$

there results six equations, namely:

$$D_4 + v_2 \cos \alpha_2 - v_1 \cos \alpha_1 = D_1 \cos(\alpha_1 + \phi_{z_1}) - D_2 \cos(\alpha_2 + \phi_{z_2}) \quad (4)$$

$$D_5 + v_2 \sin \alpha_2 - v_1 \sin \alpha_1 = D_1 \sin(\alpha_1 + \phi_{z_1}) - D_2 \sin(\alpha_2 + \phi_{z_2}) \quad (5)$$

$$D_6 + v_3 \cos \alpha_3 - v_2 \cos \alpha_2 = D_2 \cos(\alpha_2 + \phi_{z_2}) - D_3 \cos(\alpha_3 + \phi_{z_3}) \quad (6)$$

$$D_7 + v_3 \sin \alpha_3 - v_2 \sin \alpha_2 = D_2 \sin(\alpha_2 + \phi_{z_2}) - D_3 \sin(\alpha_3 + \phi_{z_3}) \quad (7)$$

$$I_1 \cos \alpha_1 + I_2 \cos \alpha_2 + I_3 \cos \alpha_3 = 0 \quad (8)$$

$$I_1 \sin \alpha_1 + I_2 \sin \alpha_2 + I_3 \sin \alpha_3 = 0 \quad (9)$$

where the following notation of symbols is used to facilitate presentation of the various expressions:

$$D_1 = I_1 Z_1 \quad (10a)$$

$$D_2 = I_2 Z_2 \quad (10b)$$

$$D_3 = I_3 Z_3 \quad (10c)$$

$$D_4 = V_1 - V_2 \cos \phi_2 \quad (10d)$$

$$D_5 = -V_2 \cos \phi_2 \quad (10e)$$

$$D_6 = V_2 \cos \phi_2 - V_3 \cos \phi_3 \quad (10f)$$

$$D_7 = V_2 \sin \phi_2 - V_3 \sin \phi_3 \quad (10g)$$

and  $\phi_{z_i}$ ,  $i = 1, 2, 3$ , are the phase angles associated with the phase impedances  $Z_i$ , exclusive of the arc resistances, which are obtained from a short-circuit test (dip test) [23]. The quantities  $D_1 \dots D_7$  can be calculated from the measured input data; since the line-to-line voltages, as well as the phase currents, are subject to measurement, the angles  $\phi_i$  are associated with the 3-phase voltage supply.

Thus, there are six equations, eqns. 4–9, which must be solved. From eqns. 4 and 5, eliminating  $v_2$  by multiplying eqn. 4 by  $\sin \alpha_2$  and eqn. 5 by  $\cos \alpha_2$ , and subtracting the resulting equations from one another yields:

$$D_5 \cos \alpha_2 - D_4 \sin \alpha_2 - v_1(\sin \alpha_1 \cos \alpha_2 - \cos \alpha_1 \sin \alpha_2) = D_1 \{\sin(\alpha_1 + \phi_{z_1}) \cos \alpha_2 - \cos(\alpha_1 + \phi_{z_1}) \sin \alpha_2\} - D_2 \{\sin(\alpha_2 + \phi_{z_2}) \cos \alpha_2 - \cos(\alpha_2 + \phi_{z_2}) \sin \alpha_2\} \quad (11)$$

Using the trigonometric identity

$$\sin(x - y) = \sin x \cos y - \cos x \sin y$$

and letting  $\alpha_1 - \alpha_2 = \alpha_{12}$ , eqn. 11 gives the following expression when solved for the arc voltage in phase 1, namely:

$$v_1 = \{D_1 \sin(\alpha_{12} + \phi_{z_1}) - D_2 \sin \phi_{z_2} + D_4 \sin \alpha_2 - D_5 \cos \alpha_2\} / (-\sin \alpha_{12}) \quad (12)$$

The arc voltage in phase 2 can similarly be obtained from eqns. 4 and 5 by eliminating  $v_1$ . To do so, multiply eqn. 4 by  $\sin \alpha_1$  and eqn. 5 by  $\cos \alpha_2$ . This results after subtracting one equation from the other and solving for  $v_2$ , in

$$v_2 = \{D_1 \sin \phi_{z_1} + D_2 \sin(\alpha_{12} - \phi_{z_2}) + D_4 \sin \alpha_1 - D_5 \cos \alpha_1\} / (-\sin \alpha_{12}) \quad (13)$$

Finally, the third arc voltage in phase 3 is obtained from eqn. 6 by solving for  $v_3$ , resulting in

$$v_3 = \{D_2 \cos(\alpha_2 + \phi_{z_2}) - D_3 \cos(\alpha_3 + \phi_{z_3}) + v_2 \cos \alpha_2 - D_6\} / \cos \alpha_3 \quad (14)$$

At this point, eqns. 12 to 14 cannot be solved for the arc voltages, since the angles  $\alpha_i$  are as yet unknown. Therefore,

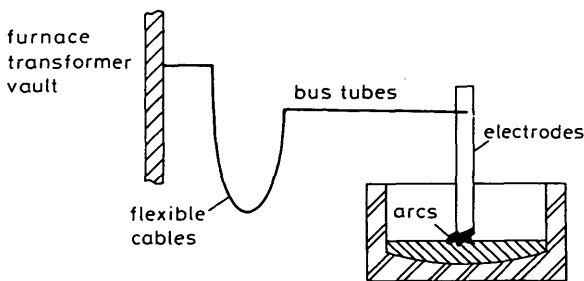


Fig. 2 3-phase electric-arc furnace and its circuit

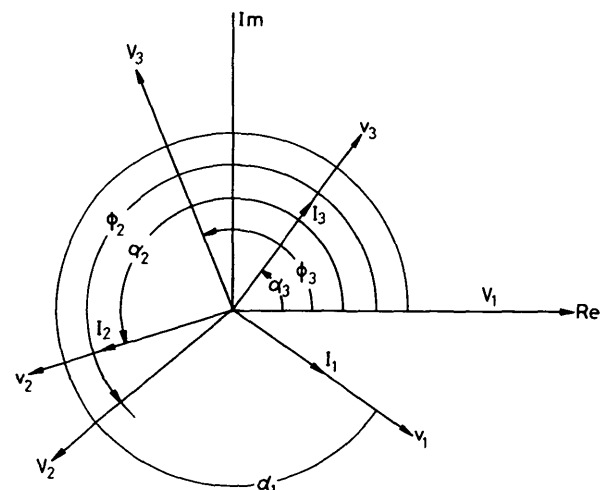


Fig. 3 Phasor diagram showing notation of variables used

expressions for these parameters will now be derived. From eqns. 4 and 5, the following relation is obtained by eliminating the  $v_1$ -term:

$$D_4 \sin \alpha_1 - D_5 \cos \alpha_1 + v_2 \sin \alpha_{12} = -D_1 \sin \phi_{z_1} - D_2 \sin (\alpha_{12} - \phi_{z_2}) \quad (15)$$

Similarly, from eqns. 6 and 7, the  $v_3$ -term is eliminated, giving

$$D_7 \cos \alpha_3 - D_6 \sin \alpha_3 - v_2 \sin \alpha_{23} = D_2 \sin (\alpha_{23} + \phi_{z_2}) - D_3 \sin \phi_{z_3} \quad (16)$$

where  $\alpha_{23} = \alpha_2 - \alpha_3$ .

Before proceeding, it should be realised that

$$\alpha_{23} = \cos^{-1} (I_1^2 - I_2^2 - I_3^2) / (2I_2I_3) \quad (17)$$

which is the cosine law applied to the current triangle, since

$$\bar{I}_1 + \bar{I}_2 + \bar{I}_3 = 0$$

Likewise, by manipulating eqns. 8 and 9, it is readily shown that

$$\sin \alpha_{23} / \sin \alpha_{12} = I_1 / I_3 \quad (18)$$

and

$$\sin \alpha_{13} / \sin \alpha_{12} = -I_2 / I_3 \quad (19)$$

Therefore, by multiplying eqn. 15 by  $\sin \alpha_{23}$  and eqn. 16 by  $\sin \alpha_{12}$ , and subtracting the resulting equations from one another, the  $v_2$ -term will be eliminated. If further simplifications are made by substituting eqns. 18 and 19 there results

$$D_4 I_1 \sin \alpha_1 / I_3 - D_5 I_1 \cos \alpha_1 / I_3 - D_6 \sin \alpha_3 + D_7 \cos \alpha_3 = D_8 \quad (20)$$

where

$$D_8 = -\{D_1 I_1 \sin \phi_{z_1} + D_2 I_2 \sin \phi_{z_2} + D_3 I_3 \sin \phi_{z_3}\} / I_3 \quad (21)$$

Eqn. 20 can now be reduced to an equation having only  $\alpha_3$  as an unknown, by letting  $\alpha_1 = \alpha_{13} + \alpha_3$  and  $\alpha_{13} = \alpha_{12} + \alpha_{23}$ . After substitution of these expressions and some trigonometric manipulation, eqn. 20 becomes:

$$-D_4 D_9 \cos \alpha_3 + D_4 D_{10} \sin \alpha_3 - D_5 D_{10} \cos \alpha_3 - D_5 D_9 \sin \alpha_3 - D_6 \sin \alpha_3 + D_7 \cos \alpha_3 = D_8 \quad (22)$$

with

$$D_9 = -I_2 \sin \alpha_{23} / I_3 \quad (23)$$

and

$$D_{10} = I_1 \cos \alpha_{13} / I_3 \quad (24)$$

Collecting terms and solving for  $\cos \alpha_3$ , gives

$$\cos \alpha_3 = (D_8 - E_1 \sin \alpha_3) / E_2 \quad (25)$$

where

$$E_1 = D_4 D_{10} - D_5 D_9 - D_6 \quad (26)$$

and

$$E_2 = -D_4 D_9 - D_5 D_{10} + D_7 \quad (27)$$

Although eqn. 25 expresses  $\cos \alpha_3$  in terms of  $\sin \alpha_3$ , the angle  $\alpha_3$  can be determined, realising

$$\sin^2 \alpha_3 = 1 - \cos^2 \alpha_3 \quad (28)$$

Substitution of eqn. 25 into eqn. 28 and solving the resulting quadratic equation in  $\sin \alpha_3$ , yields

$$\sin \alpha_3 = \{E_1 D_8 \pm E_2 (E_1^2 + E_2^2 - D_8^2)^{1/2}\} / (E_1^2 + E_2^2) \quad (29)$$

Thus, all necessary parameters can be obtained in the solution for the arc voltages as given by eqns. 12 to 14. The numerical example included will illustrate the sequence of calculations to determine the desired quantities.

### 3 Measurement data on 100 t EAF

The electrical operating characteristics for the furnace at five different transformer tap positions are presented in Table 1, which also gives the data taken during a 3-phase short-circuit test (from the so-called dip test) performed at tap no. 8.

These electrical measurements were taken by two methods. One method employed watt hour, VAr hour and V<sup>2</sup> hour measurements against time in several increments by pulsing instruments connected at the furnace transformer primary metering point for the commonly used tap positions 33 through 10. The other measurements, taken concurrently with these measurements, were monitored by means of averaging meters ( $I^2 dt$ ) in the primary-current transformer circuit.

From the acquired data of the dip test, the furnace impedance, both resistive and reactive components, on a per-phase basis, can be calculated, using either of the methods outlined elsewhere [2, 3].

From the measurement data, the arc characteristics will now be determined, such as arc power, arc length, arc resistance and arc voltage. This will be done in the following example for tap position 33, using the method described. Results for the remaining tap positions are listed in Table 2.

#### 3.1 Sample calculation

Using the method outlined, the following sequence of calculations gives the results for the arc voltages in their respective phases. The data for tap no. 33 are used (Table 1). The supply voltages on a per-phase basis are obtained from the measured primary line to line voltages and, taking  $V_1$  as the reference

Table 1: Measured furnace operating data obtained from the primary metering point

Tap position	line/line voltage (loaded)	MW	MVAr	MVA	PF	average operating reactance	Secondary currents, kA		
							Floor $I_1$	Centre $I_2$	Pit $I_3$
33	539	39.96	40.13	56.63	0.706	3.64	61.3	62.0	58.6
29	509	36.25	39.43	53.56	0.677	3.56	61.9	61.3	59.1
25	478	32.07	38.23	49.90	0.643	3.52	61.5	60.3	58.7
20	444	27.38	35.81	45.08	0.607	3.46	58.9	59.1	58.2
10	372	19.55	26.82	33.19	0.589	3.36	51.8	51.1	51.8
8*	348	3.69	38.94	39.11	0.094	3.085	64.6	72.3	61.0

\* Results for short-circuit test (no harmonics present in system and arc resistances equal to zero)

Table 2: Calculated operational values

Tap no.	$V_{ph}$		Harmonic content %	Phase reactance, m $\Omega$			Phase resistance, m $\Omega$		
	V			1	2	3	1	2	3
33	311		17	3.70	3.04	4.10	3.17	3.58	4.12
29	294		15	3.63	2.98	4.03	2.73	3.31	3.87
25	276		14	3.60	2.96	3.99	2.36	3.00	3.56
20	256		12	3.54	2.91	3.92	2.12	2.81	3.06
10	215		8.9	3.44	2.82	3.81	1.79	2.72	2.81
Phase arc voltage, V			Phase impedance m $\Omega$			Phase angle, degrees			
1	2	3	1	2	3	1	2	3	
176.1	208.8	217.0	4.87	4.70	5.81	49.4	40.3	44.9	
150.6	189.9	204.3	4.54	4.45	5.57	53.1	42.0	46.2	
126.4	168.3	185.1	4.30	4.21	5.35	56.8	44.6	48.3	
107.3	153.5	154.1	4.12	4.05	4.97	59.1	46.0	52.0	
77.2	128.3	124.4	3.88	3.92	4.73	62.5	46.0	53.6	
Phase arc power, MW			Phase resistance, m $\Omega$			Phase arc length, cm			
1	2	3	1	2	3	1	2	3	
10.79	12.95	12.71	2.87	3.37	3.71	12.2	14.9	15.5	
9.31	11.64	12.08	2.43	3.10	3.46	10.0	13.3	14.5	
7.80	10.10	10.90	2.06	2.79	3.15	8.0	11.6	13.0	
6.32	9.07	8.97	1.82	2.60	2.65	6.4	10.3	10.3	
4.0	6.56	6.44	1.49	2.51	2.40	3.9	8.2	7.9	
Phase currents, kA			% phase current unbalance			% phase arc power unbalance			
1	2	3	1	2	3	1	2	3	
61.3	62.0	58.6	1.10	2.25	-3.35	-11.19	6.58	4.61	
61.9	61.3	59.1	1.87	0.88	-2.74	-15.44	5.72	9.72	
61.5	60.3	58.7	2.22	0.22	-2.44	-18.75	5.21	13.54	
58.9	59.1	58.2	0.28	0.62	-0.91	-22.17	11.70	10.47	
51.8	51.1	51.8	0.45	-0.90	0.45	-29.41	15.76	13.65	

phasor, there results for the per-phase voltages:

$$V_1 = 539/\sqrt{3} \angle 0^\circ = 311 \angle 0^\circ \quad V_2 = 311 \angle -120^\circ$$

$$V_3 = 311 \angle 120^\circ$$

It should be noted that the impedance of the furnace transformer in the overall furnace circuit is a negligible component. Therefore, any additional unbalances which may occur owing to transformer unbalance may be neglected and the transformed voltages assumed to be a balanced set on the secondary side.

From the short-circuit test data, the average secondary reactance is 3.085 m $\Omega$ , and the individual phase reactances determined by the methods referred to earlier [2, 3] are:  $x_1 = 3.158$  m $\Omega$ ,  $x_2 = 2.594$  m $\Omega$  and  $x_3 = 3.502$  m $\Omega$ . The average secondary reactance during operation was measured to be 3.640 m $\Omega$ , as indicated in Table 1 and calculated from the data obtained during the test, as described in Section 3.

The unbalance in reactances is evident and primarily results from the unavoidable fact that the three phases cannot be constructed electrically identical. Although the outside phases can be similar, the centre arm in most designs is shorter. Furthermore, when the furnace is operating, triangulation cannot be maintained throughout the secondary system, because of, for instance, uneven electrode wear and uneven charging levels under the electrodes. This results in mutual inductance effects between phases which complicates matters.

In addition, the average secondary reactance during operation is 17% higher than that obtained during the short-circuit test. Many authors attribute this increase to the harmonic content introduced into the circuit by nonsinusoidal arc-voltage waveforms. In part, this may be true, since the arc can demonstrate preferential conduction in a form of rectification [4]. Also, the arc plasma exhibits thermal hysteresis which manifests itself by creating even harmonics. A fact that has not been considered so far is the influence of the nonlinear behaviour of the magnetic material surrounding the electrical system. All these matters combined should explain this

apparent increase in the operational reactance. To attribute the reactance increase solely to arc behaviour appears misleading at best.

Therefore, the reactances obtained from the short-circuit test are adjusted accordingly, to represent a more accurate operational value.

The phase resistances are assumed to be unaffected. Thus:

$$z_1 = 0.299 + j1.17 \times 3.158 = 0.299 + j3.695$$

$$= 3.707 \angle 85.4^\circ \text{ m}\Omega$$

similarly,

$$z_2 = 0.213 + j3.035 = 3.042 \angle 86.0^\circ \text{ m}\Omega$$

and

$$z_3 = 0.411 + j4.097 = 4.118 \angle 84.3^\circ \text{ m}\Omega$$

Knowing these parameters enables calculation of the values  $D_1$  to  $D_7$ , according to eqns. 10a-g, namely:

$$D_1 = 61\,300 \times 3.707 \times 10^{-3} = 227.2$$

$$D_2 = 188.6 \quad D_3 = 241.2$$

$$D_4 = 311 = 311 \cos(-120^\circ) = 466.5$$

$$D_5 = 269.3 \quad D_6 = 0 \quad D_7 = -538.6$$

From eqn. 17,

$$\alpha_{23} = \cos^{-1}(61.3^2 - 62.0^2 - 58.6^2)/(2 \times 62 \times 58.6) = 119^\circ$$

Likewise

$$\alpha_{12} = \cos^{-1}(58.6^2 - 61.3^2 - 62.0^2)/(2 \times 61.3 \times 62.0)$$

$$= 123^\circ$$

Therefore

$$\alpha_{13} = \alpha_1 - \alpha_2 + \alpha_2 - \alpha_3 = \alpha_{12} + \alpha_{23} = 123 + 119$$

$$= 242^\circ$$

It is now possible to determine the quantities  $D_8$  to  $D_{10}$ . From eqns. 21, 23 and 24:

$$D_8 = - [227.2 \times 61.3 \times \sin 85.4^\circ + 188.6 \times 62.0 \times \sin 86.0^\circ + 241.2 \times 58.6 \times \sin 84.3^\circ] = -676.2$$

$$D_9 = \frac{69.29}{58.56} \times \sin 119^\circ = 0.926$$

$$D_{10} = \frac{61.29}{58.56} \times \cos 242^\circ = -0.491$$

Therefore, the values of  $E_1$  and  $E_2$ , according to eqns. 26 and 27, can be obtained, giving

$$E_1 = 466.5 \times -0.491 - 269.3 \times 0.926 - 0 = -478.4$$

$$E_2 = -466.5 \times 0.926 + 269.3 \times 0.491 - 538.6 = -838.4$$

which are needed to solve for  $\sin \alpha_3$  as determined by eqn. 29, resulting in  $\sin \alpha_3 = 0.967$  and  $\alpha_3 = 75.2^\circ$ .

Although the solution to the quadratic equation gives two possible answers, the one that fits the physical situation must be chosen, which determines the sign in front of the root sign as being positive. Knowing  $\alpha_3$ , the remaining calculations are straightforward, leading to  $\alpha_2 = \alpha_3 + \alpha_{23} = 75 + 119 = 194^\circ$  and  $\alpha_1 = \alpha_2 + \alpha_{12} = 194 + 123 = 317^\circ$ , giving all the necessary values for the computation of the arc voltages according to eqns. 12–14, yielding

$$v_1 = 176.1 \text{ V} \quad v_2 = 208.8 \text{ V} \quad v_3 = 217.0 \text{ V}$$

Having determined these, the arc powers  $P_{arci} = I_i \times v_i$ ,  $i = 1, 2, 3$ , as well as the arc resistances  $R_i = v_i/I_i$ , ohmic losses in the secondary phases, total power etc. are easily found.

The remaining quantity of interest, the arc length  $l_i$ , can be determined by using established research results on electric arcs. It has been determined that, at the anode and cathode interfaces, a voltage drop of 30 V exists, and, along the arc length, a potential drop of 12 V/cm occurs; although recent published data give somewhat different values [5, 6].

From this,

$$v_{arc} = 30 + 12 l_{arc} \text{ V}$$

and the arc powers

$$P_{arc} = v_{arc} \times I_{arc}$$

Therefore, on substitution of  $v_{arc}$  into the expression for arc power and solving for the arc length, this gives

$$l_{arc} = \frac{P_{arc}}{12 I_{arc}} - 2.5 \text{ cm}$$

These values of arc length associated with the voltages are also calculated in each instance and all results are tabulated in Table 2 for each voltage tap for comparison.

From the tabulated results, it can be seen that the controlled quantity, namely the phase impedance, is fairly even on a per-phase basis. The input voltages from the furnace transformer secondary are balanced and the secondary currents are about equal. Even so, symmetry of the arcs is not guaranteed.

The existing unbalance in arc powers is not only considerable but undesirable. To balance out the power levels (in this specific case), it is necessary to decrease the arc length under

electrode no. 1 even further and/or lengthen the arc lengths for the remaining two electrodes.

An alternative method to increase the power for electrode no. 1 to the required level is to shorten the arc length in the phase which electrically follows the phase in which the higher power is desired [7]; i.e. the arc length for electrode no. 2 can be shortened for a phase sequence 123. In any event, attempting to balance the powers for the electrodes has the subsequent result of unbalancing the electrode currents. This implies unbalanced operation conditions and uneven network loading, which may even be more undesirable.

#### 4 Conclusions

The method presented makes it possible to determine the arc voltages and to obtain further information regarding the arc-circuit parameters in their respective phases. It has also been shown that to maintain equal impedance magnitudes in all phases does not necessarily imply identical phase angles, and hence arc powers. Although the phase currents are slightly unbalanced, together with highly unequal arc-circuit parameters, the arc powers are greatly unbalanced – a condition normally experienced in practice. Efforts to equalise the arc powers are generally at the expense of a current unbalance, which, in many ways, is just as undesirable from the viewpoint of network loading.

However, it is the equalisation of the arc powers at the electrode tips which is of prime concern. The need for this is well documented in the literature – namely, to ensure even wear of the refractory lining.

It is expected that, to improve power equalisation more, and thereby accepting a moderate current unbalance, the metering point must be moved from the transformer side to the electrode arms. In so doing, the large variations in electrical parameters owing to cable movements are eliminated, ensuring that more reliable information is fed to the automatic electrode-controller system.

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